

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

## Title of Invention

METHOD FOR PATTERNING A SEMICONDUCTOR REGION

Application Number :

Confirmation Number:

First Named Applicant: Sadanand Deshpande

Attorney Docket Number: FIS920040061US1

Art Unit:

Examiner:

Search string: ( 4069096 or 4310380 or 4741799 or 20030211684 ).pn

## US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4069096	1978-01-17	Reinberg et al			
	2	4310380	1982-01-12	Flamm et al			
	3	4741799	1988-05-03	Chen et al			

## US Published Applications

**Note: Applicant is not required to submit a paper copy of cited US Published Applications**

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20030211684	2003-11-13	Guo			

## Signature

Examiner Name	Date